

Strategic Wireless Applications  
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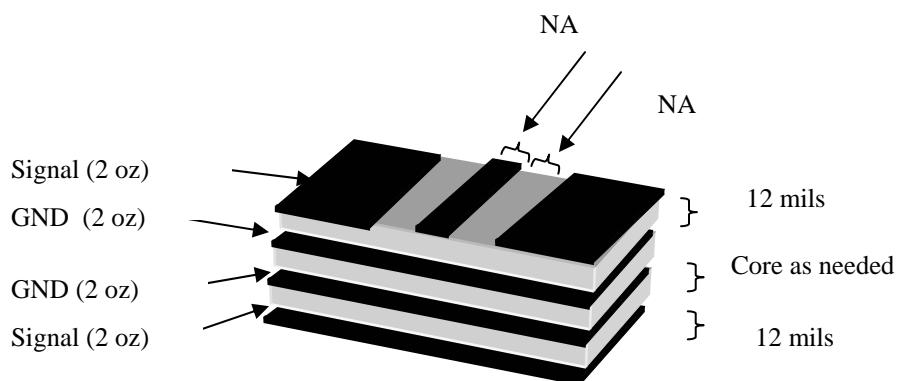
## PCB Project/Physical Description

EV Kit Part Number MAX3521EVKIT# Rev D

Quantity: 30 Turn Time: 10 working days.

Layers: 4 PCB. Thickness 0.062 +/- 10%. Dielectric: FR-406 RoHS compliant material ONLY.

RF characteristic impedance: NA Dielectric Constant:



Min. drill size (finish): 8mils. Min. line width/spacing: 6 mils/6 mils.

Plating: Lead-Free Immersion Gold Weight 2-10u" Au over 100u" Ni.

Board dimensions: 2.25" x 2.25". Surface mount pads (top) 117. Surface mount pads (bottom) 0.

Approx. # of holes: 753 Silkscreen: White. Solder Mask LPI Green. .

Scoring (location) N/A. Additional features: N/A.

### Tolerance

Board Dimensions: +/- 0.010".  
 Plated Thru Holes: +/- 0.003".  
 Pattern to Pattern: +/- 0.005".  
 Solder Mask to Pattern: +/- 0.005".  
 Legend to Legend: +/- 0.007".